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Sheet 1 of 1

<b>Form PTO-1449</b>  <b>U.S. DEPARTMENT OF COMMERCE</b> <b>PATENT AND TRADEMARK OFFICE</b>  <b>INFORMATION DISCLOSURE STATEMENT</b> <b>BY APPLICANT</b>  (Use several sheets if necessary)	<b>ATTY. DKT. NO.</b> 501.41076X00	<b>SERIAL NO.</b> Not assigned
	<b>APPLICANT</b> TAKASE, et al.	
	<b>FILING DATE</b> January 11, 2002	<b>GROUP</b> Not assigned

U.S. PAT. & TM. OFF.  
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01/11/02

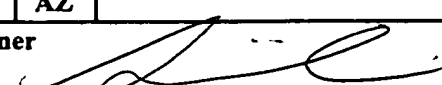
**U.S. PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
	AA					
	AB					
	AC					
	AD					
	AE					
	AF					
	AG					

**FOREIGN PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
MSA	AM	2000-78148	2000	Japan			
	AN						
	AO						
	AP						
	AQ						
	AR						
	AS						
	AT						

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

MSA	AU	"Improvement for Packet Priority of the Packet Switch with Multiple Input Queues for Every Output Port"; TAKAJYO; Technical Report of IEICE. SSE97-13, IN97-13(1997-04); pages 37-43
MSA	AV	"A Study on the Architecture of Large Scale Packet Switching System"; WAKAYAMA; Technical Report of IEICE. SSE98-160, PS98-50(1998-12); pages 67-72
MSA	AW	"A Study of Input-buffer Switch Scheduling"; SUDO, Technical Report of IEICE. SSE99-118, PS99-42(1999-12); pages 43-48
MSA	AX	"Proposal and Performance Evaluation of a Queue Length Based Weighted Fair Queueing Algorithm"; OHBA; Technical Report of IEICE. SSE96-56(1996-08); pages 79-84
	AY	
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<b>Examiner</b> 		<b>Date Considered</b> 10/21/2005